

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Houston

Serial No.: 10/085,646

Filed:

For:

02/27/02

Semiconductor Device with Fully Self-aligned Local Interconnects, and

Method for Fabricating the Device

Amendment under 37 CFR 1.111

**Assistant Commissioner of Patents** Washington, DC 20231

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)

Docket No.: TI-28032.1

Examiner: Berry, Renee R.

2818

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on

Art Unit:

Dear Sir:

The following amendments and remarks are offered in response to the Examiner's Office Action dated 09/16/02. They are respectfully submitted as a full and complete response to that Action. The Commissioner is hereby requested and authorized to charge any fees necessary for the filing of the enclosed papers to deposit account number 20-0668 of Texas Instruments Incorporated.